PLC DUTT-5000

DESCRIPTION

The Dutt-5000 is the highest performance CPU in the family of modular PLCs. It satisfies the vast majority of needs and can be used for both simple automation and the most demanding control applications. Its design allows flexible extensions at any time.

The development environment used is CoDeSys (www.3s-software.com). Dutt is part of the worldwide network of CoDeSys system partners. Today it is the most widespread IEC 61131-3-based development tool in Europe. It provides end users with comprehensive integrated solutions for simple and effective automation application project engineering. Among many of its advantages is the possibility of combining up to five programming languages.



Summary of benefits:

- Double Core ARM Cortex-A9 32-bit and double floating point precision.
- 600MHz working frequency.
- Real-time multitasking operating system.
- ✓ Virtual file system that simplifies tasks such as:
 - Automatically store the program and source code.
 - o Make backup copies of the CPU.
 - o Easily update CPU firmware.
 - o Remote connectivity and easy management over the Internet.
- Fault detection and alarm handling with history. IP addressing
- through software.

APP

The PLC (Dutt-5000) is specially designed to reduce work cycles and increase the quality of any industrial process. Its design is modular and intended for a decentralized I / O periphery.

The configuration of all the peripherals that make up the system is done in an easy and safe way through a communication bus.

The PLC (Dutt-50000) is specially designed to control good equipment, whether they are (SAIS, Motor control, Wind equipment, Solar equipment, machinery for the handling and manufacture of parts,...).

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TECHNICAL SPECIFICATIONS

CDU	
CPU	
32-bit and double floating point operations.	
600MHz.	
Europe format card.	
Firmware upgradeable.	
Operation status switch: RUN, STOP, RESET.	
One general-purpose USB 2.0 port.	
Five Ethernet ports with PC 100/10 RJ45. Field B	ous:
-PROFIBUS -PROFINET	
-EtherCAT -J1939	
-EtherNet -Modbus -IO-Link -BACnet	
	nc of LEDs
Indication of operating states of the CPU by mea Assignment of IP address via software, storage in	
Processing times	
Bit operations	25 pc
Words operations	
Integer arithmetic	
Floating point arithmetic	
Memory	500 113
EMMC internal flash	9CP
DDR	
Flash_QSPI128MB	
External SD Memory	
Retentive NVRAM Magnetoresistive Memory	
Timers / Counters	
Unlimited, each timer or counter is abstracted by	/ program
Minimum timing	
Maximum timing49	9d17h2m47s295ms
Programmable Hold Real	
Time Clock (RTC)	
Interruptions	
3 Event interrupts / 2 BUS interrupts	
Cyclical: period defined	d in milliseconds
Software event:any global program variable	
Precision watches:50	
	750□s, 500□s, 125□s
External BUS event	
I / O periphery	
Digital Channels:No pre	eset limit
Analog Channels:No pre	
Addressing the BUS of up to 12 cards	
Racks	(101/0/12)
Combined ultrafast medium buss (2 + 5)	MTC 4000
Ultra-fast long buss combo (2 + 12)	MIC-4010
Other peripherals	MT0 (00)
Motor controllers	
Interrupt card and fast counters	
5V / 60W power supply	
5V / 100W power supply	MTC-4013